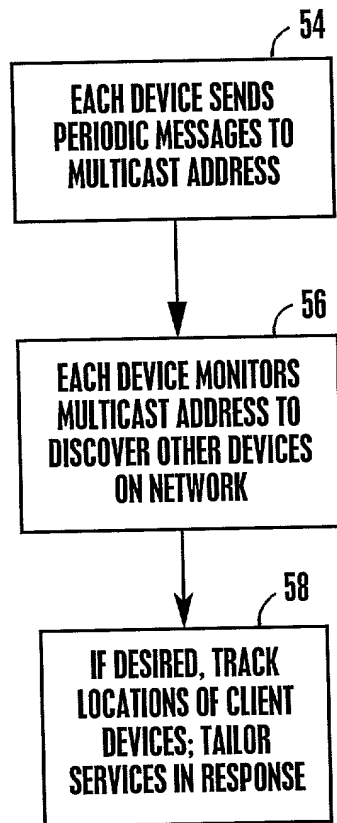


**Figure 3**  
Set-up logic



**Figure 4**  
Operation logic